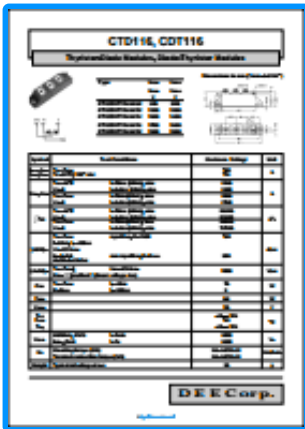
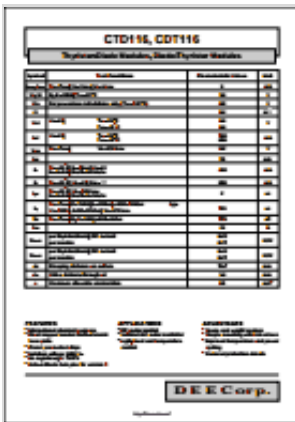


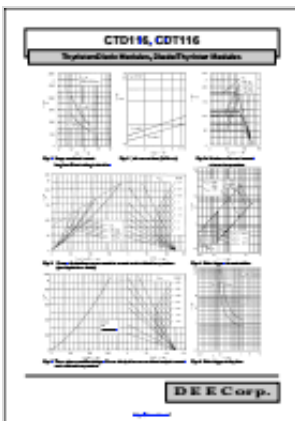
Cerca nel documento.



1



2

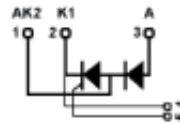


3



CTD116,

Thyristor-Diode Modules,



Type	V _{RS}	V _{DS}	V _{FM}
CTD/CDT116GK08	90	130	170
CTD/CDT116GK12	130	170	210
CTD/CDT116GK14	150	190	230
CTD/CDT116GK16	170	210	250
CTD/CDT116GK18	190	230	270

Symbol	Test Conditions	
I_{TRMS}, I_{FRMS} I_{TAVM}, I_{FVAVM}	$T_{VJ}=T_{VJM}$ $T_C=85^{\circ}C; 180^{\circ}$ sine	
I_{TSM}, I_{FSM}	$T_{VJ}=45^{\circ}C$ $V_R=0$	$t=10ms$ (50Hz), sine $t=8.3ms$ (60Hz), sine
	$T_{VJ}=T_{VJM}$ $V_R=0$	$t=10ms$ (50Hz), sine $t=8.3ms$ (60Hz), sine
$\int i^2 dt$	$T_{VJ}=45^{\circ}C$ $V_R=0$	$t=10ms$ (50Hz), sine $t=8.3ms$ (60Hz), sine
	$T_{VJ}=T_{VJM}$ $V_R=0$	$t=10ms$ (50Hz), sine $t=8.3ms$ (60Hz), sine
$(di/dt)_{cr}$	$T_{VJ}=T_{VJM}$ $f=50Hz, t_p=200\mu s$ $V_D=2/3V_{DRM}$ $I_G=0.45A$ $di_G/dt=0.45A/\mu s$	repetitive, $I_T=250A$
		non repetitive, $I_T=I_{TAVM}$
V_{DRM}	$T_{VJ}=T_{VJM};$	$V_{DRM}=2/3V_{DRM}$